EE 201 Problem Set 1 solutions

Spring 2024

Problem Set 1 covers material in Sections 1.5, 2.1, and 2.2 in the textbook.

After solving the problems, look at the solutions posted on the course website and categorize your work for each problem on the following scale:

- • Completely correct
- • Nearly correct, but made a small mathematical or copying error
- \bullet Solved part of the problem correctly
- © Started some work in the right direction
- \bigcirc Incorrect, or didn't even know where to start on the problem
- Include a question mark (?) in addition to one of the above symbols if you don't feel like you understand the question or the solution well enough to make a definite judgement.

Problems with an asterisk (*) are optional.

Binary numbers	1.a	1.b*	1.c	1.d	1.e	1.f*	1.g	1.h
Truth tables & canonical forms $2.a 2.b^* 2.c 2.d^*$								
Logic diagrams $\begin{array}{ c c c c c c c c c c c c c c c c c c c$								

What questions do you have about these concepts and skills?

What things are you uncertain about (even if you don't have a specific question)?

Approximately how long did it take you to complete the homework?

How long did you take going over the solutions and writing this reflection?

Turn in this self-assessment sheet on Gradescope. You do not need to turn in anything else, although we're happy to look at your work if you have questions!

Problem 1: Numbers

(a) Write 35 and 22 in binary, and add them.

Solution:

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35 = 32 + 2 + 1 = 100011 in binary 22 = 16 + 4 + 2 = 10110 in binary \frac{100011}{111001}
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This is 32 + 16 + 8 + 1 = 57, which checks.

(b) Write 63 and 4 in binary, and add them.

Solution:

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63 = 32 + 16 + 8 + 4 + 2 + 1 = 111111 in binary 4 = 100 in binary \begin{array}{r} 111111 \\ + 100 \\ \hline 1000011 \end{array}
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This is 64 + 2 + 1 = 67, which checks.

(c) What is the result of 01100101 + 01110110? Write the answer in both binary and decimal.

Solution:

(d) Suppose you have a circuit that can store only 6 bits, which currently has the value 20. If you add 46 to this and store the result back, what will the resulting 6-bit value be (in decimal)?

Solution:

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20 = 16 + 4 = 010100 in binary 46 = 32 + 8 + 4 + 2 = 101110 in binary 010100 + 101110 \over 1000010
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Keeping only the lower 6 digits, we have 000010 = 2 in decimal. This is called "overflow", because the number has overflowed the storage available and the result is incorrect.

(e) What is the largest positive integer you can represent with 7 bits?

Solution:

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The largest number would be 7 ones: 11111111 = 64 + 32 + 16 + 8 + 4 + 2 + 1 = 127. Notice that this is equal to 2^7 - 1.
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(f) Suppose you have a circuit that can store only 8 bits, which currently has the value 12. If you add 255 to this and store the result back, what will the resulting 8-bit value be (in decimal)? Do you notice anything interesting about the result?

Solution:

12 = 00001100 in binary

$$254 = 128 + 64 + 32 + 16 + 8 + 4 + 2 + 1 = 111111111$$
 in binary

Adding these:

00000110

+ 111111111

100001101

Keeping only the lower 8 digits, we have 00001011 = 11 in decimal. Note that this is as if we had subtracted 1 from the 12 we started with! In a few weeks we'll talk about how to represent negative numbers, and it turns out that the way to represent -1 with 8 bits is 11111111, which can also be interpreted as 255.

(g) Write the decimal number -20 in 8-bit 2's complement form.

Solution:

Writing +20 in binary is: 00010100

Inverting the bits: 11101011 Adding one: 11101100.

(h) If the number 155 were saved in an 8-bit storage location and then printed out as a signed number, what would be the result?

Solution:

Writing +155 in binary is: 10011011

Since the leftmost bit (aka MSB) is a 1, this is a negative number in 2's complement. To find the corresponding positive value:

Inverting the bits: 01100100

Adding one: 01100101 = 101 (decimal!).

Problem 2: Truth tables

(a) Write a truth table for the logic equation $Y = A \oplus \overline{B} \oplus C$.

Solution:

A	B	C	Y
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	0

(b) Write a truth table for the logic equation $Y = \overline{A \oplus B} C$.

Solution:

A	B	C	Y
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	1

(c) Write a logic equation in canonical sum-of-products and product-of-sums form for the following truth table:

A	B	$C \mid$	Y
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

Solution:

For sum-of-products form, we take the four terms with '1's in the truth table, and get $\overline{A} \, \overline{B} \, \overline{C} + \overline{A} B C + A \overline{B} \, \overline{C} + A B C$

For product-of-sums form, we take the four terms with '0's in the truth table: $(A + B + \overline{C})(A + \overline{B} + C)(\overline{A} + B + \overline{C})(\overline{A} + \overline{B} + C)$

(d) Write a logic equation in canonical sum-of-products and product-of-sums form for the following truth table:

A	B	$C \mid$	Y
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	1

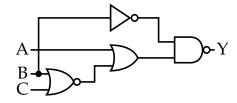
Solution:

For sum-of-products form, we take the six terms with '1's in the truth table, and get $\overline{A}\,\overline{B}C + \overline{A}B\overline{C} + A\overline{B}\,\overline{C} + AB\overline{C} + AB\overline{C} + AB\overline{C} + AB\overline{C}$

For product-of-sums form, we take the two terms with '0's in the truth table: $(A+B+C)(A+\overline{B}+\overline{C})$

Problem 3: Logic diagrams

(a) Write a logic equation representing the circuit below:

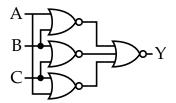


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Solution:

$$\overline{\overline{B}(A+\overline{B+C})}$$

(b) Write a logic equation representing the circuit below:



Solution:

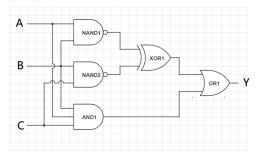
$$\overline{\overline{A} + B} + \overline{B} + \overline{C} + \overline{A} + \overline{C}$$

If you wish, this can be put in a more "standard" form using a single application of DeMorgan's theorem:

$$(A+B)(B+C)(A+C)$$

(c) Draw a logic diagram for the boolean equation $Y = (\overline{AB} \oplus \overline{BC}) + ABC$

Solution:



(d) Draw a logic diagram of the equation $AB + \overline{C}D + \overline{A}CD$ using only inverting logic (NAND/NOR/NOT). You can use whatever techniques you'd like to manipulate the equation/circuit.

Solution:

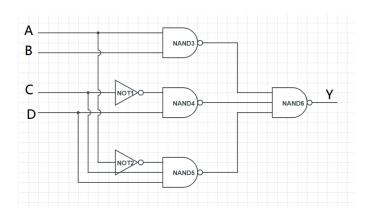
Starting with the original equation:

$$AB + \overline{C}D + \overline{A}CD$$

We have two levels of logic to invert, which can we do by applying involution and then DeMorgan's theorem:

$$= \overline{\overline{AB + \overline{C}D + \overline{A}CD}}$$

$$= (\overline{AB})(\overline{\overline{C}D})(\overline{\overline{A}CD})$$



Additional practice

These are selected problems from the textbook (at the end of each chapter) which may be helpful for practice and review. The answers to these problems are online at https://booksite.elsevier.com/9780128000564/solutions.php.

- 1.13 (binary to decimal)
- 1.25 (decimal to binary)
- 1.53 (adding binary numbers)
- 2.1 (sum-of-products form)
- 2.3 (product-of-sums form)
- 2.5 (minimizing equations)
- 2.13 (minimizing equations)
- 2.7 (equations \rightarrow circuit)
- 2.27 (bubble pushing)