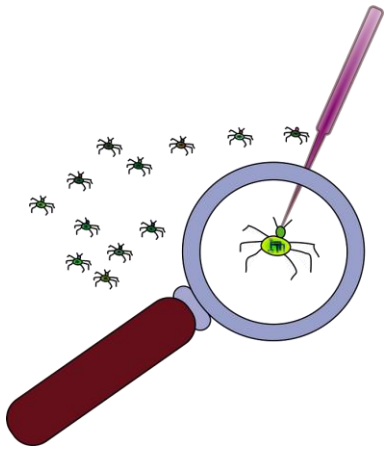


## EE165/CS147: Digital Design Verification

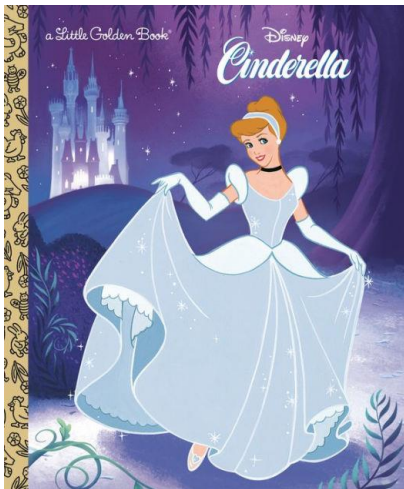
For more details, see <https://www.eecs.tufts.edu/ee/165> or e-mail [joel.grodstein@tufts.edu](mailto:joel.grodstein@tufts.edu)



Learn to debug your own designs!<sup>1</sup>

Debugging is hard 😞

But it's easier once you learn the tricks<sup>2</sup> 😊



Verification – the ugly stepchild that turns into a beautiful princess!<sup>3,4</sup>



Learn some of the most in-demand chip-design skills!<sup>5</sup>

<sup>1</sup> Your TAs and professors will love you for this

<sup>2</sup> You may think you know how to debug. You will learn *lots* more. Believe me.

<sup>3</sup> Everyone thinks verification is a waste of time. Until it's the only thing that lets you deliver a working product

<sup>4</sup> But ugly stepchildren are fine, too. Really.

<sup>5</sup> This is true. See the course syllabus for details